



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	Mail Stop Amendment
Jae-hee CHO et al.)	Group Art Unit: 2822
Application No.: 10/717,517)	Examiner: Kiesha L. Rose
Filed: November 21, 2003)	Confirmation No.: 8406
For: SEMICONDUCTOR LIGHT)	
EMITTING DIODE AND METHOD)	
FOR MANUFACTURING THE)	
SAME)	

REQUEST FOR RECONSIDERATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated November 14, 2005, Applicant respectfully requests reconsideration of the claims in light of the arguments detailed below. Claims 11, 19, and 20 are pending.

Rejections Under 35 U.S.C. §103

Claims 11, 19, and 20 were rejected under 35 U.S.C. §103(a) as unpatentable over *Uemura* (U.S. Patent No. 6,794,690) in view of *McCormick et al.* (U.S. Patent Pub. No. 2004/0119403). Applicant respectfully traverses this rejection.

In an exemplary embodiment of the invention as shown in Figs. 2-7, a semiconductor light emitting diode is comprised of an n-type semiconductor layer 20, an active layer 30, and a p-type semiconductor layer 40 that are sequentially stacked on a substrate 10. The n-type semiconductor layer 20 includes a buffer layer 21 and a first cladding layer 22, which are sequentially formed on a top surface of the substrate 10. The p-type semiconductor layer 40 includes a second cladding layer 41 and a capping layer 42, which are sequentially formed on a top surface of the